

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10538216			
<b>Filing Date:</b>	09-Jun-2005			
<b>Title of Invention:</b>	Vertical insulated gate transistor and manufacturing method			
<b>First Named Inventor/Applicant Name:</b>	Jurriaan Schmitz			
<b>Filer:</b>	Peter Zawilski			
<b>Attorney Docket Number:</b>	NL02 1416			
Filed as Large Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
Independent claims in excess of 3	1614	1	200	200
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 1 month with \$0 paid	1251	1	120	120
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>320</b>